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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Toru ISHIKAWA

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: July 8, 2004

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For

TIMING ADJUSTMENT CIRCUIT AND SEMICONDUCTOR DEVICE

INCLUDING THE SAME

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

SUBMISSION OF PRIORITY DOCUMENTS

Sir:

In accordance with 35 U.S.C. §119, Applicant confirm the prior request for priority under the International Convention and submits herewith the following documents in support of the claim:

Certified Japanese Application No.:

Japanese Patent Application No. 2002-311231 filed October 25, 2002

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on July 8, 2004

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Respectfully submitted,

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